

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10060750			
<b>Filing Date:</b>	30-Jan-2002			
<b>Title of Invention:</b>	System for controlling external models used for verification of system on a chip (SOC) interfaces			
<b>First Named Inventor/Applicant Name:</b>	Robert J. Devins			
<b>Filer:</b>	Frederick W. Gibb III/Kimberly Baumann			
<b>Attorney Docket Number:</b>	BUR9-2001-0016-US1			
Filed as Large Entity				
<b>Utility under 35 USC 111(a) Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Filing a brief in support of an appeal	1402	1	540	540
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				540